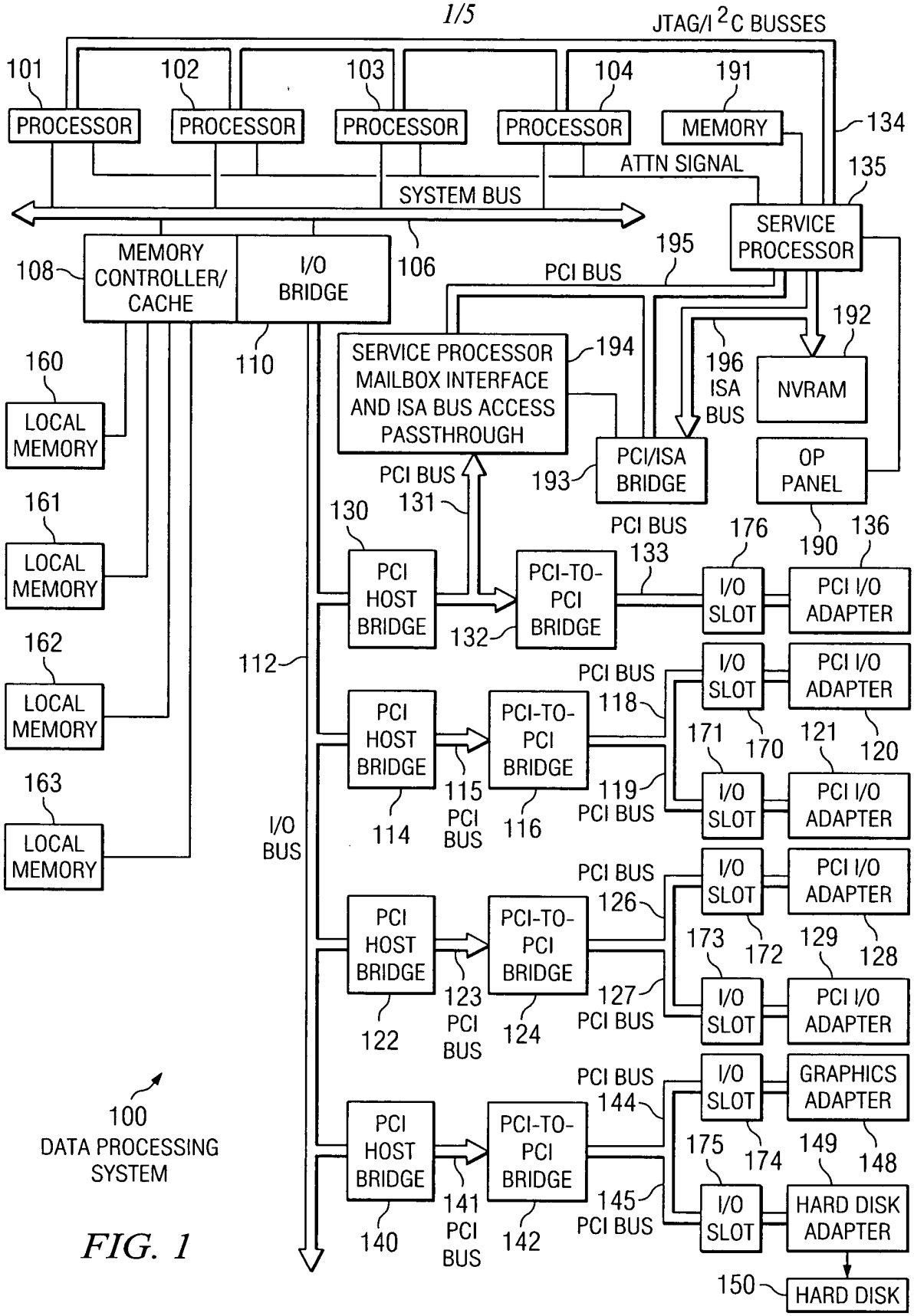
JTAG/I 2C BUSSES
101
102
103
104
191
PROCESSOR
PROCESSOR
PROCESSOR
PROCESSOR
MEMORY
134
ATTN SIGNAL
135
SYSTEM BUS
SERVICE PROCESSOR
195
PCI BUS
108
MEMORY CONTROLLER/ CACHE
I/O BRIDGE
106
192
SERVICE PROCESSOR MAILBOX INTERFACE AND ISA BUS ACCESS PASSTHROUGH
194
196 ISA BUS
NVRAM
160
LOCAL MEMORY
110
PCI BUS
193
PCI/ISA BRIDGE
OP PANEL
161
LOCAL MEMORY
130
131
PCI BUS
133
176
190
136
PCI HOST BRIDGE
PCI-TO-PCI BRIDGE
I/O SLOT
PCI I/O ADAPTER
132
162
LOCAL MEMORY
112
PCI BUS
118
I/O SLOT
PCI I/O ADAPTER
PCI HOST BRIDGE
PCI-TO-PCI BRIDGE
171
170
121
120
163
LOCAL MEMORY
115 PCI BUS
119 PCI BUS
I/O SLOT
PCI I/O ADAPTER
I/O BUS
114
116
PCI BUS
126
I/O SLOT
PCI I/O ADAPTER
PCI HOST BRIDGE
PCI-TO-PCI BRIDGE
173
172
129
128
123 PCI BUS
127 PCI BUS
I/O SLOT
PCI I/O ADAPTER
122
124
PCI BUS
144
I/O SLOT
GRAPHICS ADAPTER
PCI HOST BRIDGE
PCI-TO-PCI BRIDGE
175
174
149
148
141 PCI BUS
145 PCI BUS
I/O SLOT
HARD DISK ADAPTER
140
142
150
HARD DISK
100
DATA PROCESSING SYSTEM

FIG. 1

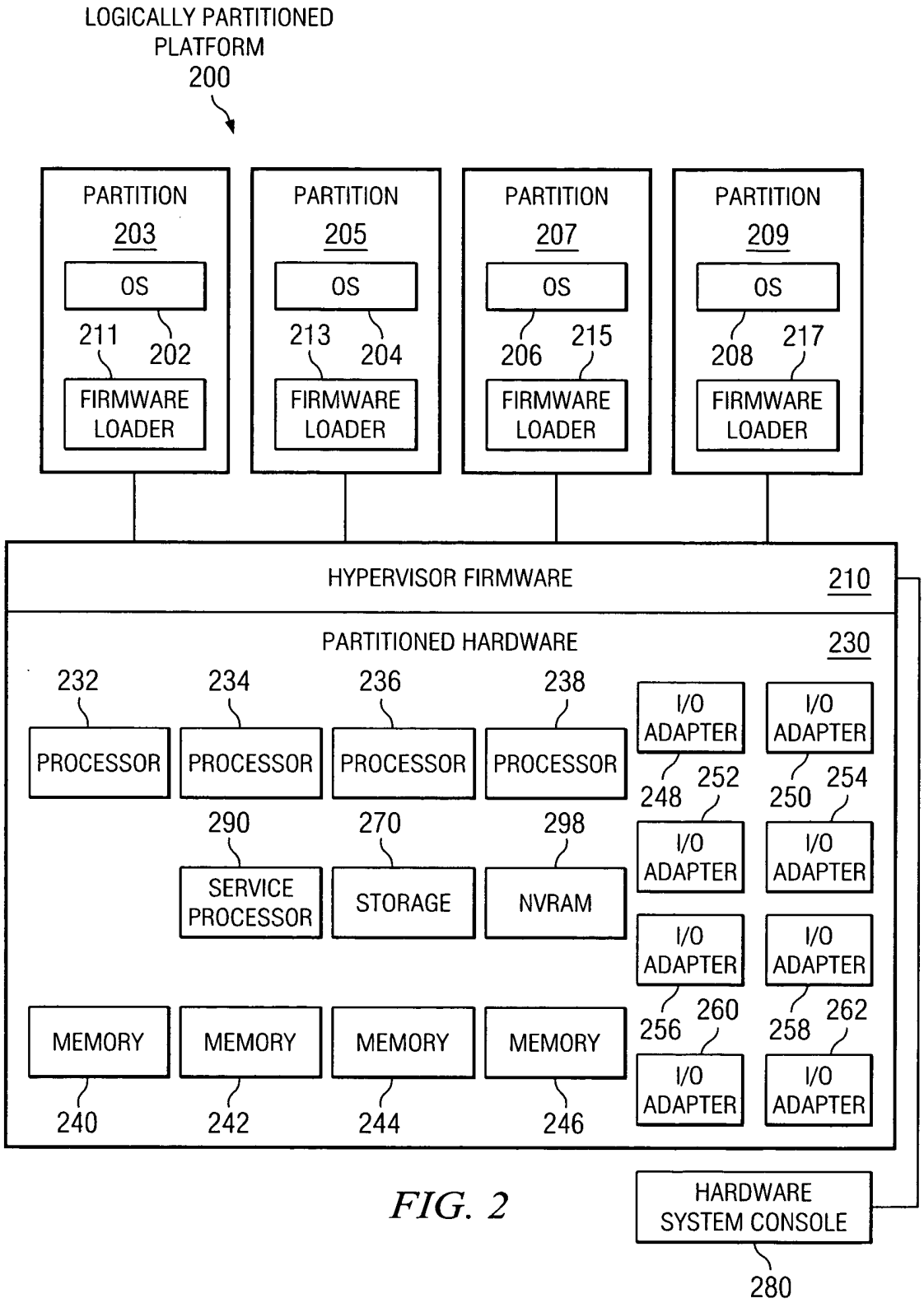
LOGICALLY PARTITIONED PLATFORM
200
PARTITION
203
OS
211
202
FIRMWARE LOADER
PARTITION
205
OS
213
204
FIRMWARE LOADER
PARTITION
207
OS
206
215
FIRMWARE LOADER
PARTITION
209
OS
208
217
FIRMWARE LOADER
HYPERVISOR FIRMWARE
210
PARTITIONED HARDWARE
230
232
234
236
238
PROCESSOR
PROCESSOR
PROCESSOR
PROCESSOR
I/O ADAPTER
I/O ADAPTER
248
252
250
254
I/O ADAPTER
I/O ADAPTER
290
270
298
SERVICE PROCESSOR
STORAGE
NVRAM
I/O ADAPTER
I/O ADAPTER
MEMORY
MEMORY
MEMORY
MEMORY
256
260
258
262
I/O ADAPTER
I/O ADAPTER
240
242
244
246
HARDWARE SYSTEM CONSOLE
280

FIG. 2

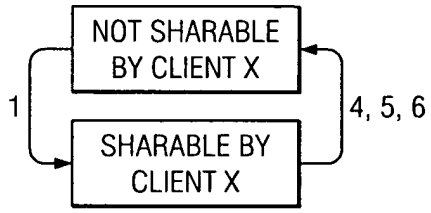

FIG. 3A

OPERATIONS	
1	H_GRANT_LOGICAL
2	H_ACCEPT_LOGICAL
3	H_RETURN_LOGICAL
4	H_RESCIND_LOGICAL (NORMAL)
5	H_RESCIND_LOGICAL (FORCED)
6	Server_Partition_Failure
7	Client_Partition_Failure

FIG. 3B

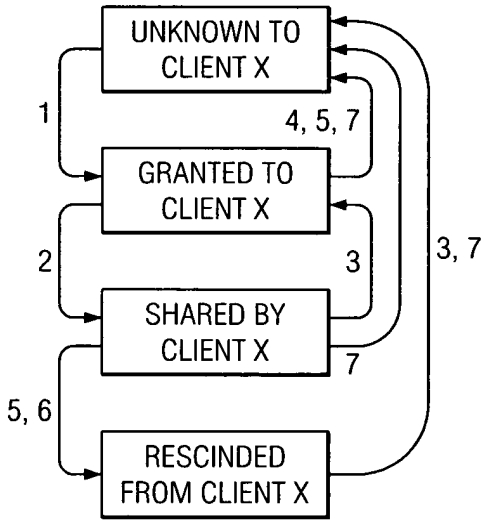

FIG. 3C

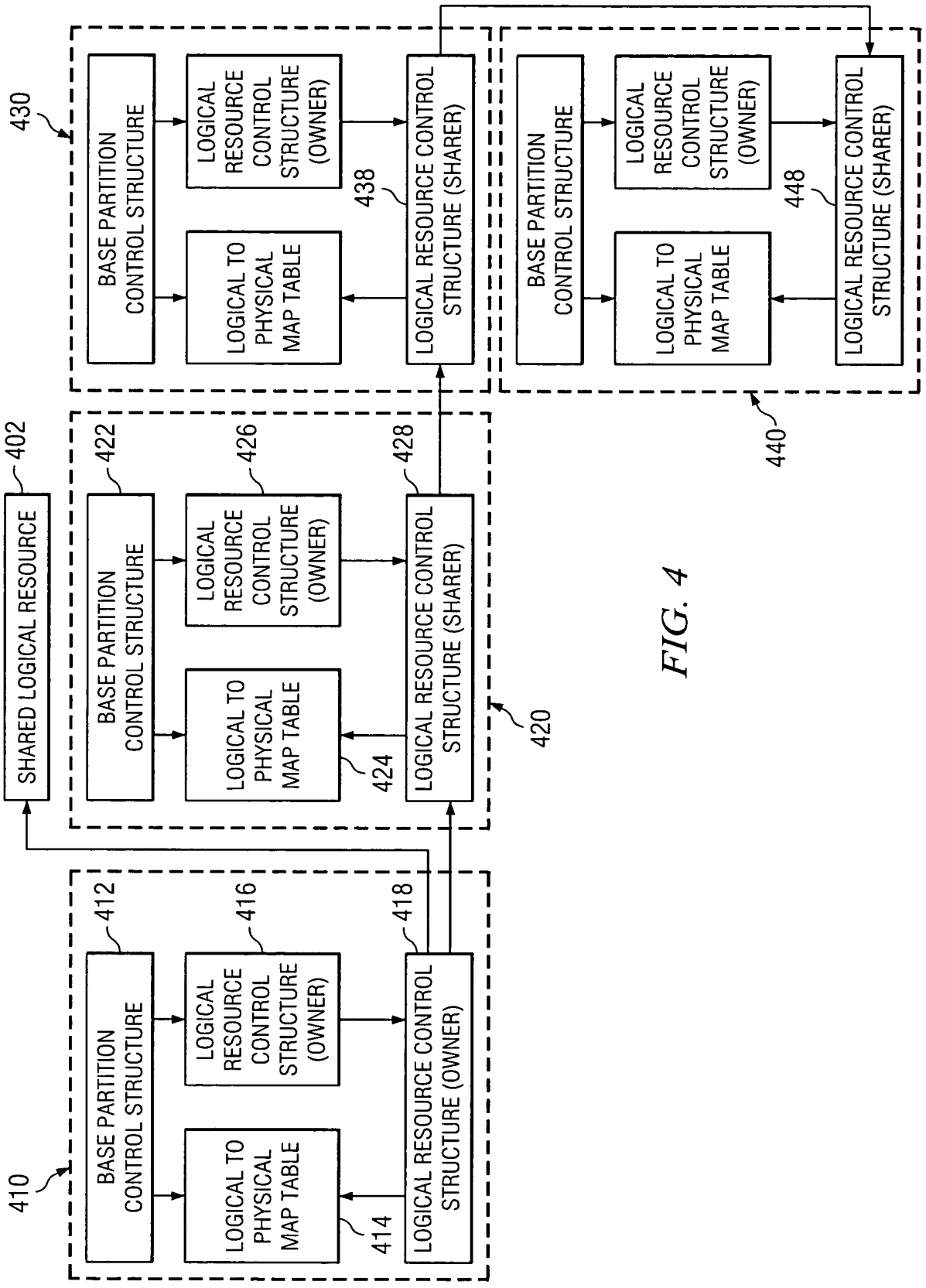
430
BASE PARTITION CONTROL STRUCTURE
LOGICAL RESOURCE CONTROL STRUCTURE (OWNER)
LOGICAL TO PHYSICAL MAP TABLE
438
LOGICAL RESOURCE CONTROL STRUCTURE (SHARER)
BASE PARTITION CONTROL STRUCTURE
LOGICAL RESOURCE CONTROL STRUCTURE (OWNER)
LOGICAL TO PHYSICAL MAP TABLE
448
LOGICAL RESOURCE CONTROL STRUCTURE (SHARER)
440
402
SHARED LOGICAL RESOURCE
422
BASE PARTITION CONTROL STRUCTURE
426
LOGICAL RESOURCE CONTROL STRUCTURE (OWNER)
LOGICAL TO PHYSICAL MAP TABLE
424
428
LOGICAL RESOURCE CONTROL STRUCTURE (SHARER)
420
410
412
BASE PARTITION CONTROL STRUCTURE
416
LOGICAL RESOURCE CONTROL STRUCTURE (OWNER)
LOGICAL TO PHYSICAL MAP TABLE
414
418
LOGICAL RESOURCE CONTROL STRUCTURE (OWNER)

FIG. 4

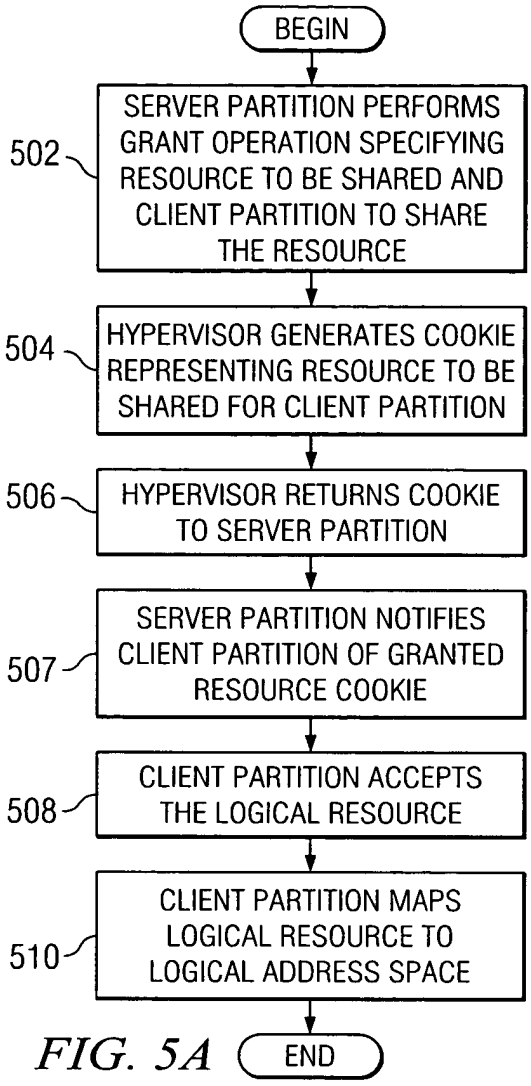

FIG. 5A

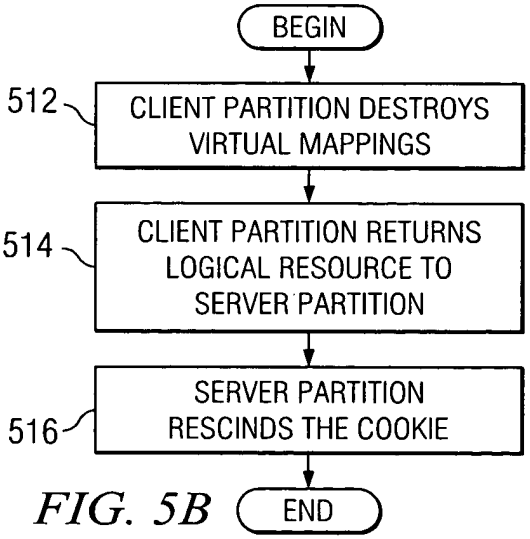

FIG. 5B

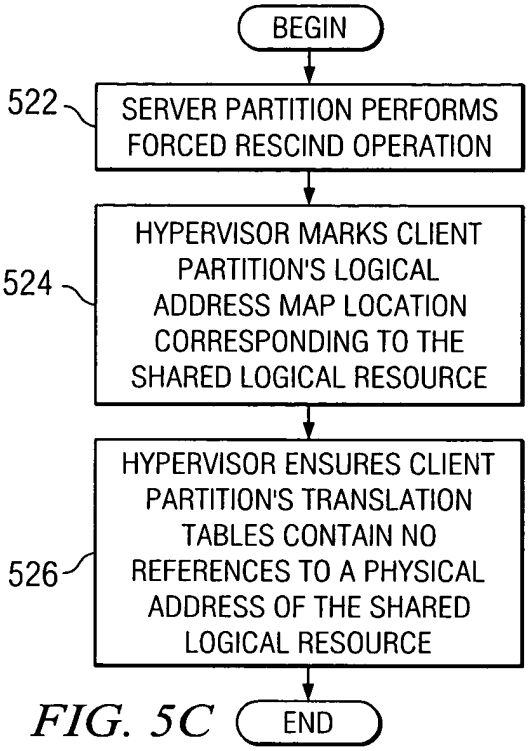

FIG. 5C

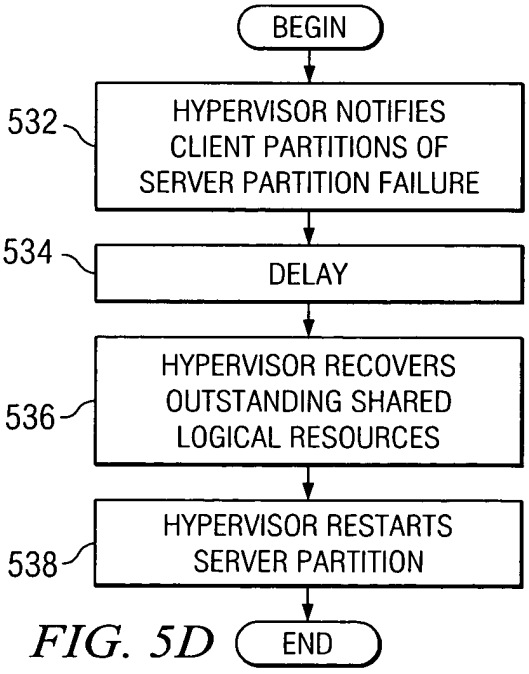

FIG. 5D